



PTO/SB/08A (10-01)
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Substitute for form 1449A/PTO		Complete if Known	
		Application Number	09/815,921
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Filing Date	March 24, 2001
		First Named Inventor	Bindiganavale S. Nataraj
		Art Unit	2661
		Examiner Name	Not Yet Assigned
Sheet 1	of 7	Attorney Docket Number	NLM.P002

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No. 1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code 2 (if known)			
CLD		US- 3,257,646	06-21-1966	Roth	
CLD		US- 3,353,159	11-14-1967	Lee, III	
CLD		US- 3,602,899	08-31-1971	Lindquist et al.	
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CLD		US- 3,685,020	08-15-1972	Meade	
CLD		US- 3,868,642	02-25-1975	Sachs	
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Examiner Initials	Cite No. 1	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code 3 - Number 4 - Kind Code 5 (if known)			
CLD		EP 0 267 612 A2	05-18-1988	Akita	
CLD		EP 0 381 249 A2	08-08-1990	Akashi	
CLD		EP 0 660 332 A1	06-28-1995	Montoye	
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CLD		WO 98/12651	03-26-1998	Kempke	
CLD		WO 99/31581	06-24-1999	Therkildsen	

Examiner Signature	<i>Arthur Z. Beck</i>	Date Considered	12/7/05
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		Application Number	09/815,921
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Filing Date	March 24, 2001
		First Named Inventor	Bindiganavale S. Nataraj
		Group Art Unit	2661
		Examiner Name	Not Yet Assigned
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Sheet	5	of	7

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T²
CLD		Advanced Micro Devices, "Final Am99C10A 256 x 48 Content Addressable Memory" Publication No. 08125, Rev. G, December 1992, pp. 1-21.	
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CLD		PANKAJ GUPTA et al., "Routing Lookups in Hardware at Memory Access Speeds", Proc. Infocom, April 1998, 8 pgs.	
CLD		PANKAJ GUPTA and NICK MCKEOWN, "Packet Classification on Multiple Fields", SIGCOMM (September 2, 1999), 14 pages.	
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CLD		HIROSHI KADOTA et al., "An 8-kbit Content-Addressable and Reentrant Memory", IEEE Journal of Solid-State Circuits, Vol. SC-20, No. 5, October 1985, pp. 951-956.	

Examiner Signature	<i>[Signature]</i>	Date	17/7/02
Signature		Considered	

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Sheet	6	of	7

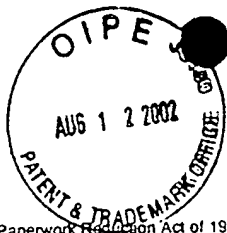
OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
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CUD		KLSI KE5B064A1 Address Processor Ver. 2.0.2 Preliminary (believed to be published late 1995 or early 1996).	
CUD		KLSI KE5B064H Series Address Processor Ver. 1.0.1 (believed to be published in late 1995 or early 1996).	
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CUD		T.V. LAKSHMAN and D. STILIADIS, "High-Speed Policy-Based Packet Forwarding Using Efficient Multi-Dimensional Range Matching", SIGCOMM, 1988, pp. 203-214.	
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Examiner Signature		Date Considered	12/7/05
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CLD		MUSIC Semiconductors, Advance Information, "MUAA Routing CoProcessor (RCP) Family", (October 1, 1998), Rev. 1b, pages 1-16.	
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CLD		Stardust.com, "WhitePaper -- Introduction to QoS Policies", 1999, pp. 1-22.	
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